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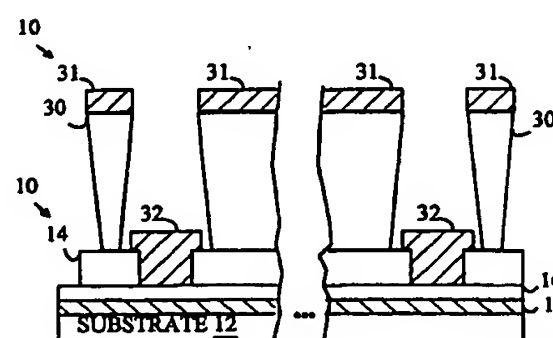
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(54) Method for fabricating pixelated polymer organic light emitting devices

(57) A display[10,40] having a plurality of addressable pixels and a method for constructing the same. The display[10,40] includes a substrate[12] having a first conducting layer[13] thereon. The conducting layer includes a plurality of parallel row electrodes having edges separated by insulating material. A first polymer layer[16, 160] constructed from one or more thin films overlies the parallel row electrodes. An insulating layer[14] overlies the polymer layer, the insulating layer[14] having windows[15] therein defining pixels for the display[10,40]. The insulating layer[14] overlies the edges of the row electrodes while providing access to the first polymer layer[16, 160] in the area of the pixels. A second conducting layer[32] overlies the insulating layer[14] and includes a plurality of separated column electrodes. Each column electrode crosses a plurality of the row electrodes and is located over the windows[15] in the insulating layer[14] at points defining the pixels. The second conducting layer[32] is preferably constructed by depositing a layer of photoresist over the insulating layer[14] and patterning the layer of photoresist to provide openings extending therethrough. The openings are wider in the portion of the layer nearest to the insulating layer[14]. This layer acts as a shadow mask for depositing a layer of conducting material. In one embodiment of the invention, a second polymer layer[161] is located between the insulating layer[14] and the second conducting layer[32]. The insulating layer[14] is preferably constructed from a layer of photoresist that is baked to cross-link the photoresist. The polymer layers include an electroluminescent material,

which is preferably based on a polyfluorene.



EP 0 951 073 A2

Description

Field of the Invention

[0001] The present invention relates to electroluminescent devices, and more particularly, to the fabrication of organic light emitting diodes for use in matrix-addressed displays

Background of the Invention

[0002] Organic polymer-based electroluminescent devices (OLEDs) have the potential for providing inexpensive alternatives to alphanumeric displays and x-y addressable displays. Typically, an OLED consists of a transparent substrate coated with a transparent conducting material, such as Indium Tin oxide (ITO), one to five organic layers and a cathode made by evaporation or sputtering a metal of low work action characteristics, such as Ca or Mg. The organic layers are chosen so as to provide charge injection and transport from both electrodes to the electroluminescent organic layer (EL) where charges recombine emitting light. Usually there are one or two organic hole transport layers (HTL) between the ITO and EL, as well as one or two electron injection and transporting layers (EL) between the cathode and the EL.

[0003] OLED's are fabricated usually by either vacuum depositing the organic layers (typically dyes of low molecular weight) onto the substrate, or by spin casing from solutions in the liquid state. In the following discussion, a vacuum deposited dye device will be referred to as "D-OLED", and a polymer-based device will be referred to as a "P-OLED". In some devices, a mixture of both techniques is utilized.

[0004] To define pixels and other patterns (such as icons) on a display, the ITO, organic layers, and cathode must be patterned. The patterning of the ITO layer may be carried out by conventional photolithographic techniques.

[0005] However, the patterning of the organic layers and the cathode presents difficulties that inhibit the commercial development of OLEDs. In particular, it is difficult to obtain high-resolution patterns by vapor deposition of materials through shadow masks. In order to circumvent this problem, typical D-OLED's use a "self patterning" approach in which two layers of photoresist are patterned on ITO using microlithography to create a topology that interrupts the continuity of the dye and cathode films into discrete rows (or columns) when the cathode is deposited. While this methodology provides high-resolution patterns, the construction of multi-color displays by this technique presents problems. In particular, designs in which different color pixels are constructed by depositing different light emitting dyes are difficult to construct.

[0006] Spin cast polymers allow the generation of multi-color displays in which different pixels are con-

structed from different polymers. Systems for photolithographically patterning the various polymer layers have been developed for some of these polymers. For example, the patterning of organic polymers for use in generating multi-color displays is taught in PCT application PCT/GB97/00995. However, the deposition of the cathode in P-OLED's is typically carried out by depositing the cathode through a shadow mask. This shadow mask process has lower resolution than the photolithography-based methods used in D-OLEDs.

[0007] A second problem with P-OLED devices is the tendency of the anode and cathode to short through the thin polymer layers. The polymer layers are typically a 0.1 μm . The ITO layer over which the polymer is cast is also approximately this thick. A P-OLED device is typically constructed by etching column electrodes of ITO, covering the ITO columns with the relevant color polymers by one or more spin casting operations, and then depositing row electrodes for the cathode. The quality of the polymer layers at the points at which the layers step up and down from the ITO anodes to the spaces between the ITO anodes is often less than perfect. In particular, pin-holes or other breaks in the polymer layer are sometimes present at these points. When the cathode layer is deposited in rows over the polymer layer, shorts form between the cathode and underlying ITO anode. These shorts result in poor device yield.

[0008] Broadly, it is the object of the present invention to provide a improved method for constructing P-OLED displays.

[0009] It is a further object of the present invention to provide a method for constructing P-OLED displays that allows the resolution obtained in photolithography-based methods to be obtained in the cathode electrodes.

[0010] It is a still further object of the present invention to provide a method for constructing P-OLED displays which avoids the shorting problem between the anode and cathode electrodes.

[0011] These and other objects of the present invention will become apparent to those skilled in the art from the following detailed description of the invention and the accompanying drawings.

Summary of the Invention

[0012] The present invention is a display having a plurality of addressable pixels and a method for constructing the same. The display includes a substrate having a first conducting layer thereon. The conducting layer includes a plurality of parallel row electrodes having edges separated by insulating material. A first polymer layer overlies the parallel row electrodes. An insulating layer overlies the polymer layer, the insulating layer having windows therein defining pixels for the display. The insulating layer overlies the edges of the row electrodes while providing access to the first polymer layer in the area of the pixels. A second conducting layer overlies

the insulating layer and includes a plurality of separated column electrodes. Each column electrode crosses a plurality of the row electrodes and is located over the windows in the insulating layer at points defining the pixels. The second conducting layer is preferably constructed by depositing a layer of photoresist over the insulating layer and patterning the layer of photoresist to provide openings extending therethrough. The openings are wider in the portion of the layer nearest to the insulating layer. This layer acts as a shadow mask for depositing a layer of conducting material. In one embodiment of the invention, a second polymer layer is located between the insulating layer and the second conducting layer. The insulating layer is preferably constructed from a layer of photoresist that is baked to cross-link the photoresist. The polymer layers include an electroluminescent material, which is preferably based on a polyfluorene and polyphenylenevinylene (PPV).

Brief Description of the Drawings

[0013]

Figure 1 is a top view of a portion of a display according to the present invention after the deposition of a windowed insulating layer.

Figure 2 is a cross-section of the display shown in Figure 1 through line 20-21.

Figure 3 is a cross-section of the display shown in Figure 1 through line 22-23.

Figure 4 is a top view of the display shown in Figure 1 after the tapered photoresist layer has been deposited.

Figure 5 is a cross-sectional view of the display shown in Figure 4.

Figure 6 is a cross-sectional view of a portion of a display according to the present invention in which the polymer layer is split into two layers.

Detailed Description of the Invention

[0014] The present invention gains its advantages through the use of insulating layer having windows that define the active pixel areas. The insulating layer separates the cathode from the anode in the areas overlying the edges of the anode. Hence, this layer prevents shorting through imperfections in the polymer film in the vicinity of the edges of the anode electrodes. In addition, the insulating layer tends to planarize the active emissive regions thereby reducing step coverage problems in depositing the cathode. Finally, the insulating layer provides a base for the construction of a mask

structure that defines the cathode.

[0015] The manner in which a display according to the present invention is constructed can be more easily understood with reference to Figures 1-3. Figure 1 is a top view of a portion of a display 10 after the insulating layer discussed above has been deposited and patterned. Figure 2 is a cross-section of display 10 through line 20-21. Figure 3 is a cross-section of display 10 through line 22-23. Display 10 is constructed on a transparent substrate 12. A layer of a transparent anode material such as indium tin oxide is deposited on substrate 12 and etched to form the anode electrodes 13 that connect all of the pixels in a row.

[0016] Those polymer layers that can withstand the temperatures and processing steps utilized to deposit the insulating layer 14 are deposited over the anode electrodes by one or more spin casting operations to form a layer 13. It should be noted that layer 13 may include regions having different polymers in the case of a color display. That is, the regions of polymer layer 13 that emit different colors may be patterned separately using photolithographic techniques. Typically a hole transport layer that is the same for all pixels is deposited over the anode. An EL layer is then patterned on top of the HTL. If more than one color pixel is present, the EL layer for each color is deposited separately and patterned such that the different color pixels have different EL materials. Finally, an ETL layer is deposited over the EL layer. Since the deposition and patterning of the individual polymer layers for pixelated displays is known in the art and not relevant to the present invention, a detailed discussion of these processing steps will not be given here. A number of different systems for generating polymer layers having different color emitting regions have been published. The reader is directed to PCT application PCT/GB97/00995 mentioned above and Strum and Wu, "INTEGRATED ORGANIC LIGHT EMITTING DIODE STRUCTURES USING DOPED POYMERS", Society for Information display Conference Proceedings, September 15-19, 1997, Toronto, Canada for examples of such patterning systems. These publications are hereby incorporated by reference. For the purposes of the present discussion various polymer layers will be treated as a single, or double, layer having various components.

[0017] After the polymer layer has been deposited, an insulating layer 14 is deposited over the device and patterned to form windows 15 therein. The windows overlie the anode electrodes to define the light-emitting region of each pixel. It should be noted that the windows do not overlie the edges 17 of the anodes 13. Hence, any imperfections in the polymer film arising from the spin casting of the polymer layers are separated from the anode by the insulating layer.

[0018] In the preferred embodiment of the present invention insulating layer 14 is a layer of photoresist which is cross-linked after being patterned by baking. For example, layer 14 may be constructed from conven-

tional positive photoresists such as AZ-240 (Hoeschst Corp., AZ photoresist products, Sumerville, NJ). The photoresist is photolithographically patterned by exposure to UV through a photomask and then developed with AZ400K. After the material in the window region has been removed, the device is baked at 140°C for 30 minutes. It will be apparent to those skilled in the art from the preceding discussion that polymer layer 13 must be able to withstand the processing steps needed to deposit, develop and cure insulating layer 14.

[0019] If any of the component layers from which layer 13 is constructed cannot withstand these conditions, the layers are deposited over insulating layer 14 after layer 14 has been deposited and cured. In this case layer 13 is split into two layers. The first layer includes the HTL and any of the component polymer layers that can withstand the deposition of insulating layer 14 up to the first layer that cannot withstand these conditions. The remaining layers are then deposited over insulating layer 14. The order of the component layers making up layer 13 must, of course, be maintained if layer 13 is to be so divided.

[0020] The deposition of as many layers as possible under the insulating layer provides improved film quality. The thickness of the ITO layer is typically 0.15 μm or less. Hence, the steps in the anode that must be covered when the polymer layers are spun onto the anode are relatively modest compared to the step in the insulating layer which is typically 1 μm . The additional step height can result in imperfections in the polymer layers that are spun on top of the insulating layer.

[0021] In addition to withstanding the baking of insulating layer 14, the portion of layer 13 that underlies layer 14 must also be resistant to the chemical processing steps inherent in depositing layer 14. In particular, the HTL material must have good adhesion to ITO.

[0022] The sub-layers of layer 13 deposited under layer 14 must also be compatible with the photoresist deposition. In particular, these layers must be resistant to the solvents used with the photoresist and must have good lesion to photoresist. In the preferred embodiment of the present invention, the photoresist uses water-based solutions during the development thereof. In addition, resistance to acetone, isopropyl alcohol, and saturated ammonia vapor is required. Finally, the materials of these layers must be resistant to exposure to light, both visible and UV. Here "resistant" means that the layer maintains its mechanical integrity, resistance to lifting off, and electronic characteristics (charge transport and luminance) through the process.

[0023] The HTL layer can be constructed from one or more polymer layers that are insoluble in the photoresist solvents. Polymers that are not soluble in xylenes, water, isopropyl alcohol, or acetone are preferred. For example, the HTL layer may be constructed from poly(3,4-ethylenedioxythiophene) (PEDOT). An EL layer constructed from a polymer blend based on polyfluorenes and PPV derivatives is preferred. This poly-

mer is soluble in xylenes, and is not soluble in water, acetone, or isopropyl alcohol. The combined thickness of the two polymers is typically 150 nm.

[0024] As noted above, other layers of polymer materials may be deposited over insulating layer 14 if necessary. Insulating layer 14 is typically 1 μm . While this thickness is sufficient to partially planarize the device, it is not so thick as to substantially interfere with the spin casing of other layers of polymer. For the purposes of the present discussion, it will be assumed that no such additional layers are needed, as the polyfluorene-based polymers discussed above are sufficiently resistant to the photoresist deposition process to withstand the construction of layer 14 thereon.

[0025] As noted above, one problem with prior art P-OLED displays is the shadow mask system used to deposit the cathode electrodes. The present invention avoids this problem by utilizing a tapered photoresist mask which, in effect, provides a shadow mask disposed very close to the surface of the device. The metal for the cathode electrodes is then deposited through this mask. The resolution of the resulting cathode electrodes is set by the resolution of the photolithographic process used in depositing and developing the tapered photoresist.

[0026] Refer now to Figures 4-5. Figure 4 is a top view of a portion of display 10 after the tapered photoresist layer 30 has been deposited. Figure 5 is a cross-sectional view of display 10 similar to that shown in Figure 2 after the tapered photoresist layer and cathode material have been deposited. The cathode material 31 is deposited by evaporation over tapered photoresist layer 30. The tapered photoresist layer allows the deposition of the cathode layer 31 in a manner that provides a cathode 32 that is not in contact with the tapered photoresist. Accordingly, the photoresist can be removed without subjecting the cathode conductors to stresses that could interrupt the cathode conductors.

[0027] The construction of a tapered photoresist layer is known to the art, and hence, will not be discussed in detail here. The construction of such tapered or overhanging layers is discussed in detail in Hatzakis, *et al.*, "Single-Step Optical Lift-Off Process", *IBM J. Res. Develop.*, 34, pp. 452-458, 1980. In the preferred embodiment of the present invention, the same photoresist material as that utilized for insulating layer 14 is utilized; however, the material is deposited to a thickness of approximately 7 μm . The layer is patterned by exposure to a broad band UV source. The exposed resist is first baked in an oven at 90°C in vacuum and annealed in NH_3 . The resist is then removed from the NH_3 , exposed to UV, and then developed in an alkaline solution. The degree of taper produced is determined primarily by the initial exposure energy and the developer concentration. In the preferred embodiment of the present invention, a 75° taper is utilized.

[0028] As noted above, polymer layers that can withstand the baking of insulating layer 14 are preferably

deposited prior to the deposition of insulating layer 14. However, if one or more of the polymer layers cannot withstand the high baking temperature, layer 16 can be split into two layers as shown in Figure 6. Figure 6 is a cross-sectional view of a portion of a display 40 in which the polymer layer is split into two layers shown at 160 and 161. To simplify the drawing, those elements that serve the same function as elements discussed above with respect to display 10 have been given the same numeric designations. Polymer layer 160 includes the portion of the polymer layer stack that can withstand the baking temperatures utilized in the deposition of insulating layer 14. After the insulating layer is completed, the remaining polymer layers are deposited as shown at 161. The tapered photoresist layer 30 is then deposited on top of polymer layer 161. The cathode material 31 is then deposited through the tapered photoresist layer to produce the cathode electrodes shown at 32.

[0029] The insulating window discussed above has additional advantages in systems in which the underlying polymer layers are deposited as thin "stripes". One method for fabricating multi-color displays is to "print" the EL polymer layers as thin stripes using a conventional printing technique. Each color corresponds to a stripe of a different EL polymer. This procedure provides a means for generating regions having different color polymers in one step, as opposed to the repeated patterning and deposition steps required to lay down the EL polymer regions by photolithographic methods. Unfortunately, the EL polymer strips obtained by printing are very thin and tend to lift off of the underlying layers. The insulating windows utilized on the present invention provide a means for preventing such lift-off, since the insulating material covers the edges, and hence, pins the edges down.

[0030] Various modifications to the present invention will become apparent to those skilled in the art from the foregoing description and accompanying drawings. Accordingly, the present invention is to be limited solely by the scope of the following claims.

Claims

1. A display[10,40] having a plurality of addressable pixels, said display[10,40] comprising: a substrate[12] having a first conducting layer[13] thereon, said conducting layer comprising a plurality of parallel row electrodes having edges separated by insulating material; a first polymer layer[16, 160] overlying said parallel row electrodes; an insulating layer[14] overlying said polymer layer, said insulating layer[14] having windows[15] therein defining pixels for said display[10,40], said insulating layer[14] overlying said edges of said row electrodes, wherein said windows[15] provide access to said first polymer layer[16, 160]; and a second conducting layer[32] comprising a plurality of separated column electrodes, each column electrode crossing
- a plurality of said row electrodes and being located over said windows[15] in said insulating layer[14] at points defining said pixels.
2. The display[10,40] of Claim 1 further comprising a second polymer layer[161] located between said insulating layer[14] and said second conducting layer[32].
3. The display[10,40] of Claim 1 wherein said insulating layer[14] comprises a layer of photoresist.
4. The display[10,40] of Claim 1 wherein said first polymer layer[16, 160] comprises a hole transport material.
5. The display[10,40] of Claim 1 wherein said first polymer layer[16, 160] comprises an electroluminescent material chosen from the group consisting of polyfluorene and PPV.
6. The display[10,40] of Claim 1 wherein said first polymer layer[16, 160] comprises a hole transport material.
7. A method for constructing a pixelated display[10,40] comprising a plurality of addressable pixels, said method comprising the steps of: patterning a conducting layer deposited on a substrate[12] into a plurality of parallel row electrodes[13] having edges separated by insulating material; depositing a first polymer layer[16, 160] overlying said parallel row electrodes; depositing an insulating layer[14] overlying said polymer layer, said insulating layer[14] having windows[15] therein defining pixels for said display[10,40], said insulating layer[14] overlying said edges of said row electrodes, wherein said windows[15] provide access to said first polymer layer[16, 160]; and depositing a second conducting layer[32] comprising a plurality of separated column electrodes, each column electrode crossing a plurality of said row electrodes and being located over said windows[15] in said insulating layer[14] at points defining said pixels.
8. The method of Claim 7 further comprising the step of depositing a second polymer layer[161] located between said insulating layer[14] and said second conducting layer[32] after depositing said insulating layer[14].
9. The method of Claim 7 wherein said insulating layer[14] comprises a layer of photoresist.
10. The method of Claim 9 further comprising the step of baking said layer of photoresist to a temperature sufficient to cross-link said photoresist.

11. The method of Claim 7 wherein said first polymer layer[16, 160] comprises a hole transport material.
12. The method of Claim 7 wherein said first polymer layer[16, 160] comprises an electroluminescent material comprising a polyfluorene. 5
13. The method of Claim 7 wherein said step of depositing said second conducting layer[32] comprises the steps of: 10
- depositing a layer of photoresist[30] over said insulating layer[14] and patterning said layer of photoresist to provide openings extending therethrough, said openings being wider in the portion of said layer nearest to said insulating layer[14]; and depositing a layer of conducting[31] material through said patterned layer of photoresist. 15
14. The method of Claim 13 wherein said layer of photoresist has a thickness greater than 7 μm . 20

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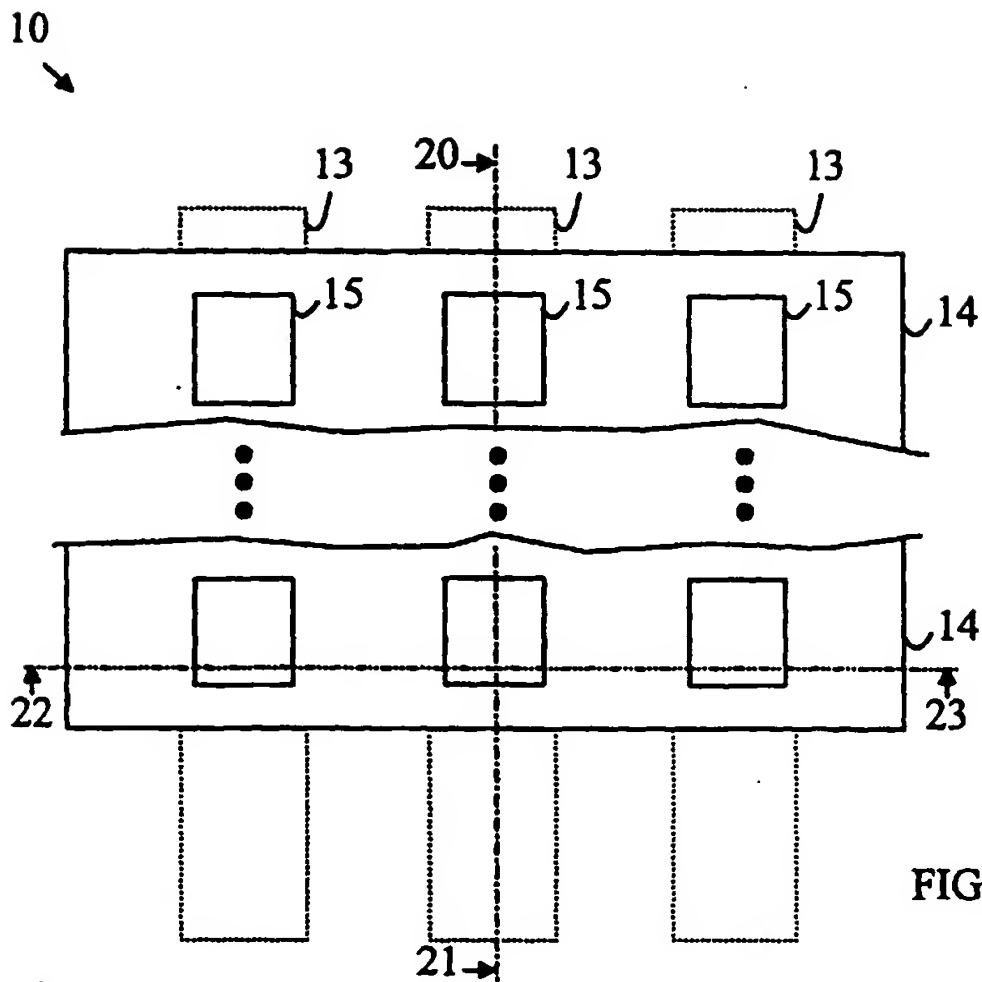


FIGURE 1

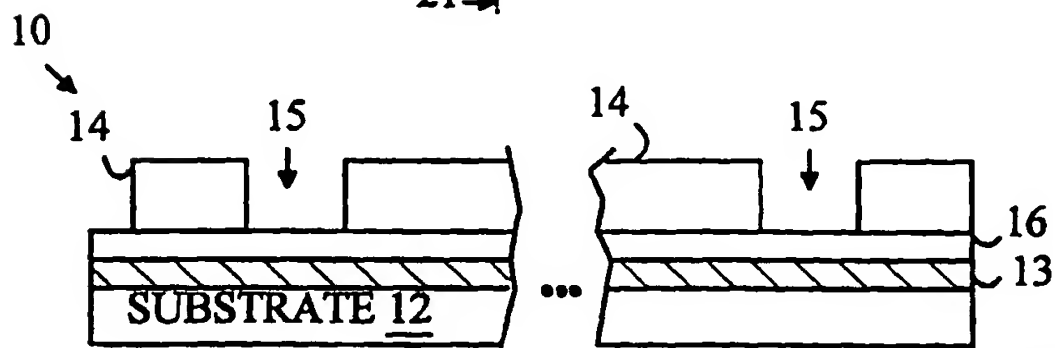


FIGURE 2

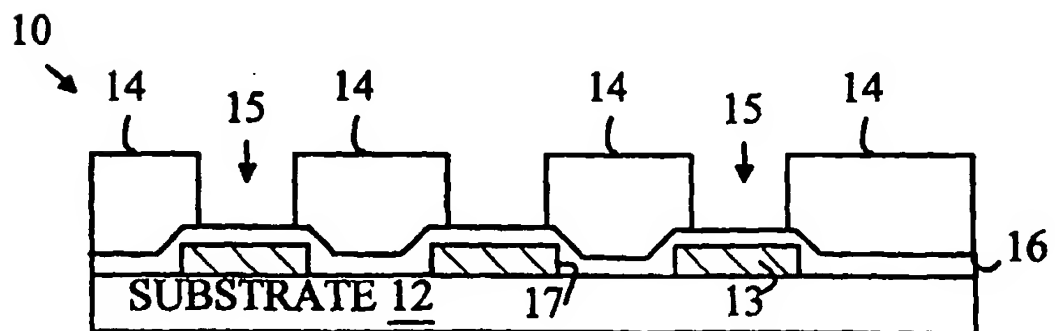


FIGURE 3

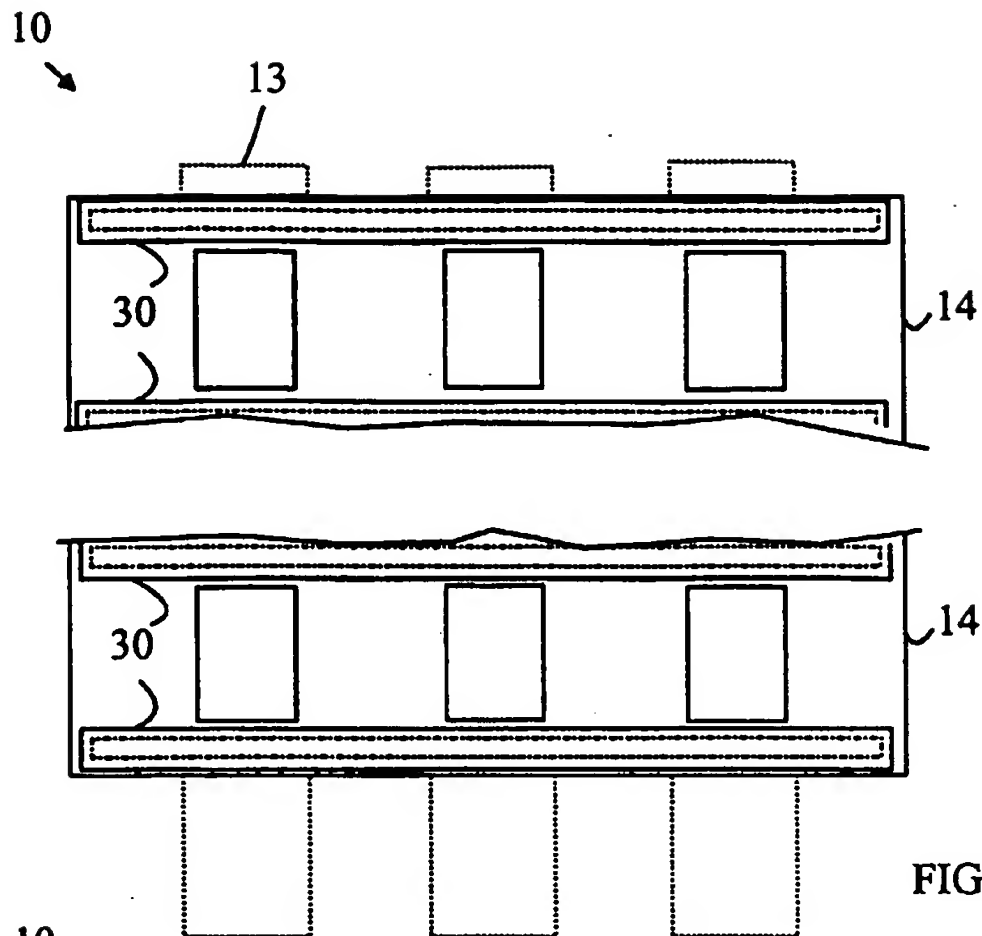


FIGURE 4

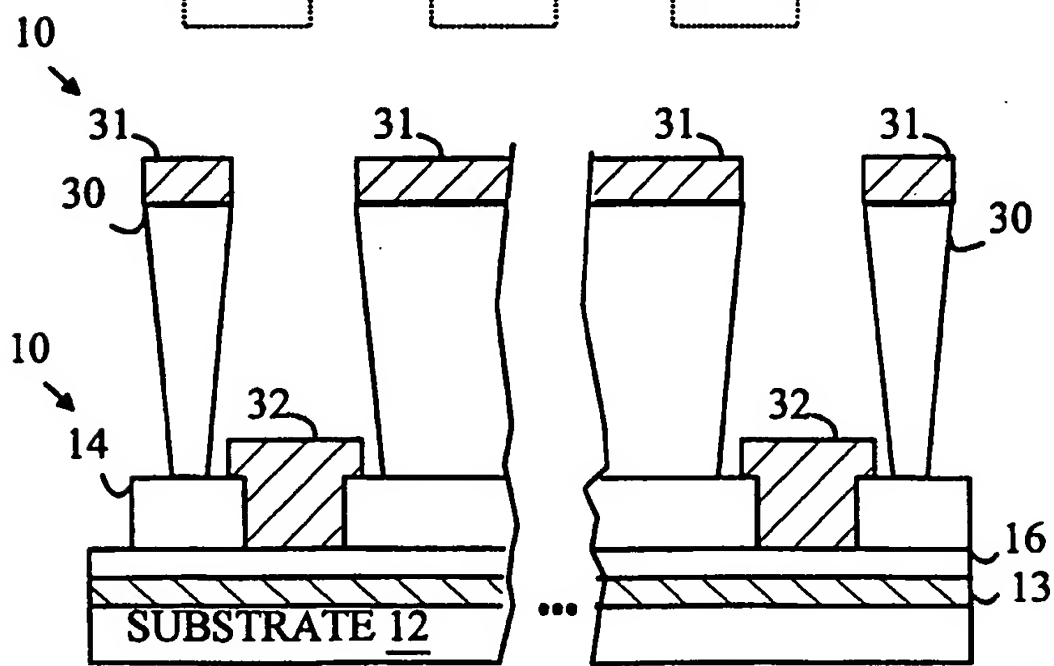


FIGURE 5

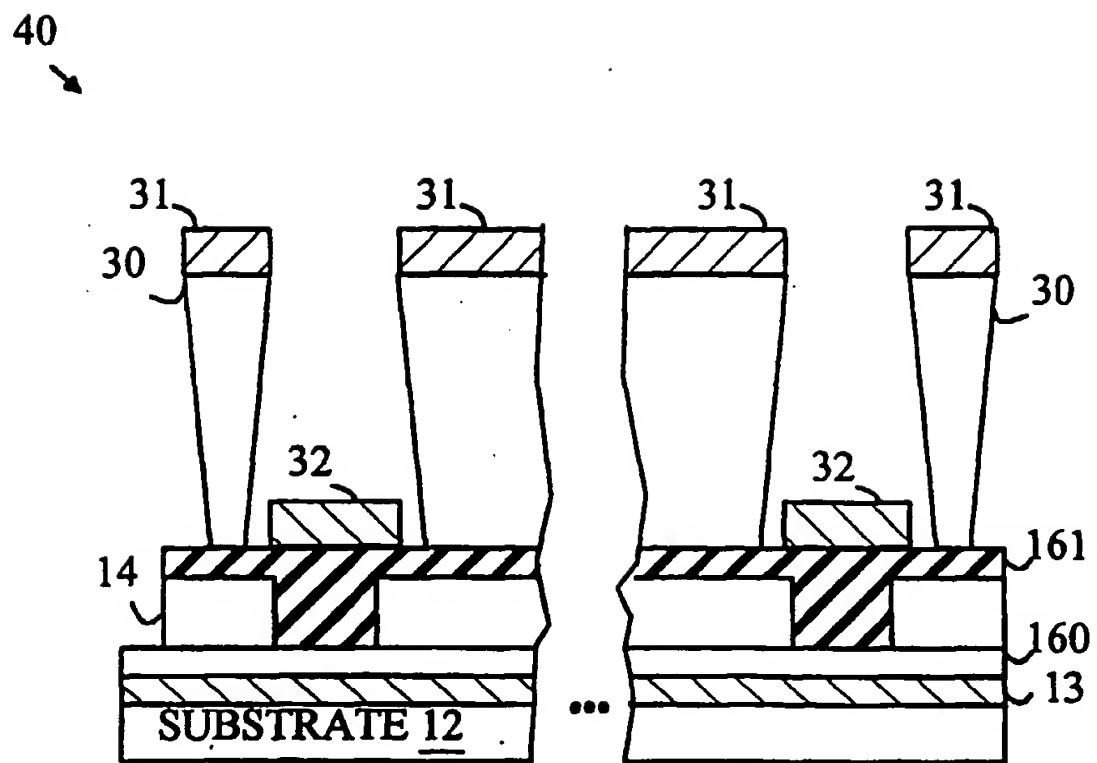
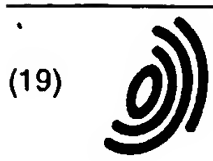


FIGURE 6



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layer[14] is preferably constructed from a layer of photoresist that is baked to cross-link the photoresist. The polymer layers include an electroluminescent material, which is preferably based on a polyfluorene.

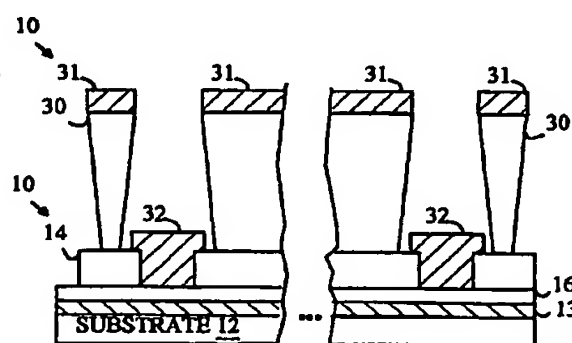


FIGURE 5

EP 0 951 073 A3



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EUROPEAN SEARCH REPORT

Application Number
EP 98 12 0666

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			H01L
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 22 January 2001	Examiner van der Linden, J.E.
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**ANNEX TO THE EUROPEAN SEARCH REPORT
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This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on
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